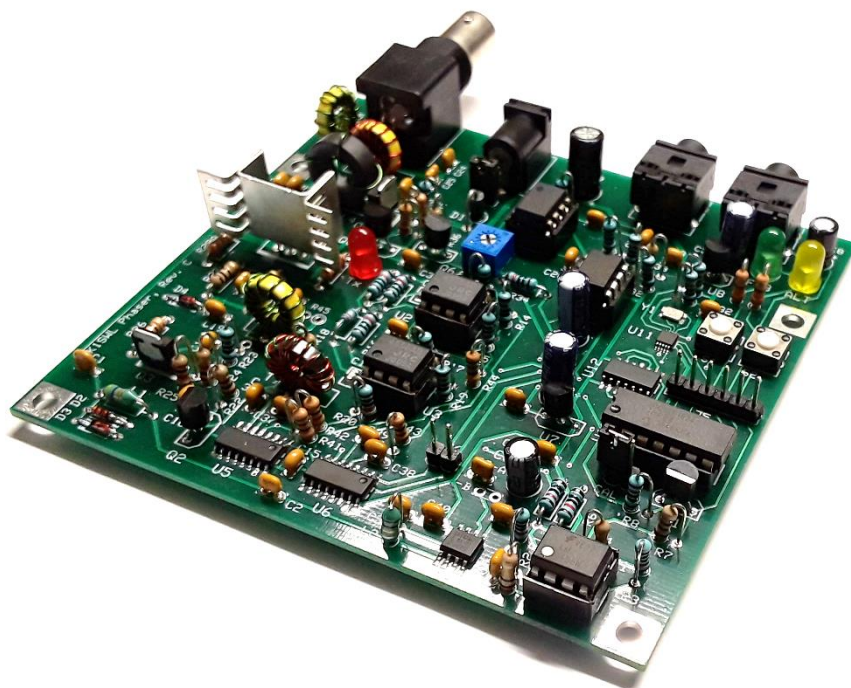


PHASER

Digital Mode Transceiver

*A single-board, 4-watt, Digital Mode SSB transceiver
for use on 160, 80, 60, 40, 30, 20 or 17 meters*

by Dave Benson, K1SWL



INSTRUCTION MANUAL (30M Version)

Revision C7
(Nov 2020)

Produced by Midnight Design Solutions

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Allows the Phaser instruction manual, when printed as a double-sided document, to show the Group Assembly Instructions on the left (the even page) with the corresponding Group Component Placement on the right (the odd page) ... like an open book. This avoids the need to flip pages when viewing corresponding pictorials.

Note: *This instruction manual describes the assembly and checkout of the Phasers starting in Round 4 Production*

(#300 and up) The PCBs for this version are identified by the silkscreen text

'K1SWL Phaser, Rev.C' on the left edge of the PCB

For a Summary of Round 4 Changes, see page 37

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INTRODUCTION

The **Phaser Digital Transceiver™** is a single-board, 4-watt SSB radio specifically designed for using digital modes with computers running WSJT-X and FLDIGI applications. The **Phaser** is capable of transmitting and receiving on the 160m, 80m, 60m, 40m, 30m, 20m or 17m amateur bands. Each of these monoband transceivers is programmed to operate first at the popular FT8 frequencies, while a pushbutton entry shifts the operating frequency to an alternate frequency of the user's choice, initially provided at the JS8 'watering hole'. This ALT frequency may be easily reprogrammed to be anywhere in the HF spectrum, thus providing frequency flexibility to accommodate other digital modes such as PSK31, Feld Hell, Olivia, SSTV, etc. To ensure efficient use of spectrum in operation, the transmitter features an **adjustment-free phasing single-sideband (SSB) design**. The use of precision components provides unwanted sideband suppression in excess of 30 dB across its operating range, thus minimizing inadvertent QRM to other operators on the (unused) lower sideband. The use of SSB also eliminates the issue of out-of-phase signal cancellation at the Phaser's direct-conversion receiver. As such, it allows **Phaser** users to communicate with each other. This is a clear advantage over the use of 'entry-level' double-sideband (DSB) transceivers.

SPECIFICATIONS

Frequency coverage*:

Phaser-30 board: 10.136 MHz (FT8) and 10.130 MHz (JS8/Alt)

* FT8 frequency is hard-coded

* Alt frequency is soft-coded for JS8 mode, user reprogrammable

Transmit:

- 4 Watts – nominal
- Phasing SSB -- all power into one sideband (USB).

Receive: MDS of -109 dBm

Frequency Calibration: 1-time

User Programmable ‘Alt’ frequency: 100 kHz to 30MHz, 1 kHz resolution

Adjustments: Tx Drive

DC Power: 12V @ 130ma (Rx), 1A (Tx), center-positive 2.1mm coaxial plug

Dimensions: 4.125” x 3.85” x 1.20”

Kits: Currently available for 80m, 60m, 40m, 30m, 20m and 17m

Components:

- All through-hole parts for user assembly
- Employs only seven surface mount parts, all pre-installed

Enclosure Kits: Optionally available

PREPARATION FOR ASSEMBLY

Take a moment to carefully read this section, as it provides good “starting point” guidance that can be of great help in building the Phaser.

Tools & Supplies:

You’ll need the following tools and supplies for assembling the Phaser:

- [] Soldering iron – 20 to 40W, preferably thermostatically controlled
- [] Fine 60/40 (Pb/Sn) rosin core solder
- [] Diagonal cutters
- [] Needle-nose pliers
- [] Small flat- blade screwdriver
- [] Adhesive (Scotch®) tape
- [] (Helpful) Close-up glasses or magnifier
- [] Operational Needs: Power supply, antenna, audio cables, computer and WSJT-X or FLDIGI apps

General Assembly Notes:

- **A number of components are polarity-sensitive.** This includes all semiconductor devices and diodes, and the five electrolytic capacitors. These are contained in antistatic envelopes.
- **Components these days are tiny!** In sunlight, one can read their printed values with +3.0 reading glasses. Most of the time, though, builders can benefit from using a 10-power eye loupe (costing about \$3-4 from DigiKey).
- **Assembly sequence:** These assembly instructions provide a step-by-step guide to successful construction of the Phaser. It is recommended that you follow the **six** grouped assembly sequences in order.
- **Schematic and Component Placement diagrams** are provided in Appendices 2 and 3, respectively. It is highly recommended to print a copy of this manual for reference during construction. As you build, you can check off each construction step as you complete them in order.
- **Further details** may be found in the grouped assembly sequences.

Parts Organization:

- Take some time to organize the parts provided and check them against the Parts List shown in Appendix 1. You may want to organize parts in a muffin tin or insert them into a sheet of Styrofoam® to keep them from disappearing... especially if you have a cat.
- To minimize the chance of static damage, keep ICs and semiconductors in the anti-static package until you’re ready to install them. As a practical matter, you don’t need an antistatic mat or ESD wrist strap. **Note:** You should try to avoid setting these components down on paper.
- The Phaser parts kit has been thoughtfully prepared on a number of individually-bagged small cards containing all parts that will ultimately be attached to the printed circuit board. Now would be a good time to inventory the parts on these cards against the Parts List shown later in this manual in Appendix 1. If parts are missing in your kit, send an email to n2apb@midnightdesignsolutions.com and he will promptly provide shortages/replacements.

Phaser Parts:

Refer to the Parts List shown in Appendix 1 for inventorying all parts.



As received, these two bags contain the 9 cards shown below. (Sample shown is for Phaser-40.)
Stack 'em up in a small box, plastic drawer, etc to provide easy access to the parts when called for in the Instructions!

Phaser Enameled Wire		
#28 AWG twisted-pair (red/grn)	#26 AWG (red)	#22 AWG (grn)
Midnight Design Solutions, N2APB Design by K1SWL, Kitting by K3PEG		

Phaser Semiconductors, 1 of 2	
D1	1N5818
D2, D3, D4	1N4148
D5	1N4756
D6	Green LED
D7	Yellow LED
Q1, Q2, Q6	2N4401
Q3	2SC5706
Q7	MPS751
U1, U4	MC1458P
Midnight Design Solutions, N2APB Design by K1SWL, Kitting by K3PEG	

Phaser Semiconductors, 2 of 2	
U2, U3	NJM4556
U7	78L05
U8	78L33
U9	LM393
U10	16F1824
U13	NE602AN
Midnight Design Solutions, N2APB Design by K1SWL, Kitting by K3PEG	

Phaser Resistors, 1 of 2	
R29A, R29B	1 Ohm 5%
R26	10 Ohms 5%
R41-R44	51 Ohms 5%
R7, R22, R27	100 Ohms 5%
R45	330 Ohms 5%
R21, R24, R25, R28, R33, R36-R38	1K Ohms 5%
R1, R3, R5, R6, R8, R9, R11-17, R19, R20, R30, R32, R34, R35, R39, R40	10K Ohms 1%
Midnight Design Solutions, N2APB Design by K1SWL, Kitting by K3PEG	

Phaser Resistors (& Inductors), 2 of 2	
R31	12K Ohms 5%
R18	60.4K Ohms 1%
R2, R4	100K Ohms 5%
R10	10K Ohms potentiometer
Inductors	L4: T50-43 (grey) 5 turns of #22 (grn)
L4 and T1	T1: T37-43 (grey) 14 turns of #28, twisted pair (red/grn)
Midnight Design Solutions, N2APB Design by K1SWL, Kitting by K3PEG	

Phaser Capacitors	
C3a	10 pF
C3b	150 pF
C5	330 pF
C4, C9-C11, C14-C17, C19-C21, C26, C27, C30-C32, C34, C35-C39, C46	0.1 uF
C6, C7, C8, C28, C33	10 uF pol. aluminum
C29	220 uF pol. aluminum
Midnight Design Solutions, N2APB Design by K1SWL, Kitting by K3PEG	

Phaser Connectors	
2-pin male headers	2-pin female jumpers
2x3-pin male header: J6	
3.5mm stereo jacks: J3, J4	
DC Power Jack: J2	
BNC RF Connector: J1	
Midnight Design Solutions, N2APB Design by K1SWL, Kitting by K3PEG	

Phaser Miscellaneous	
14-pin IC socket	
8-pin IC sockets	
S1, S2 push-btn switches	
Bumper, adhesive	
T0-220 heatsink and 4-40 hardware	
Midnight Design Solutions, N2APB Design by K1SWL, Kitting by K3PEG	

Phaser 40m band specific parts	
C1	47 pF
C2	220 pF
C18, C22	330 pF
C23	820 pF
C24	470 pF
C25	100 pF
L1	10 uH
L2	2.2 uH
L3	1.5 uH
L5: T50-2 (red) 14 turns of #26	
L6: T50-2 (red) 17 turns of #26	
Q4	
Midnight Design Solutions, N2APB Design by K1SWL, Kitting by K3PEG	

GROUP 1 ASSEMBLY: Board Power

Refer to the diagram “Group 1 Component Placement” on the next page for reference to the location of components being installed here.

NOTE: The location of components installed in this assembly group are shown with either a **green dot** (for the ceramic capacitors) or a yellow dot (for all others).

[] **IC Sockets:** Install the five 8-pin IC sockets and the 14-pin IC socket. Tape each of these sockets down on the top side of the board to hold them in place before soldering. Observe the orientation as shown on the silkscreen (i.e., the printing on the board). Ensure that all socket leads protrude through the board before soldering. It’s helpful to solder a pair of opposite corner-pins and check to make sure the sockets are solidly seated on top of the board. Retouch as needed.

[] **J2:** Install DC power connector J2. It may be helpful to tape it down before turning the board over to solder.

[] **2-pin Pinheader:** Install a 2-pin male header at the location next to D1. Place a 2-pin jumper on the header just installed.

[] **D1:** To install diode D1 vertically, bend the lead on the BANDED end of the 1N5818 so the component forms a ‘hairpin’ shape. Install the component with the body of the component oriented as shown in the photo. The diode’s body is closest to the male header and jumper installed in the previous step.

The silkscreen symbol  indicates that a component’s leads are formed into a hairpin prior to installation. See the photo on the right. The straight component lead gets inserted to the center of the circular silkscreen designator, while the lead that is bent over gets inserted into the other hole.



[] **0.1 uF ceramic capacitors:** Install all 23 0.1 uF capacitors (marked as ‘104’) at the locations shown on the next page. These locations are shown in **green**. **NOTE:** The capacitor designator ‘C40’ on the board is a typo and should be ‘C46’, as is the schematics and parts lists.

[] **U7:** Install the 78L06 3-pin IC (U7), matching its orientation to that of the silkscreen outline.

[] **U8:** Install the 3-pin 78L33 regulator at U8, again matching its orientation to that of the silkscreen outline.

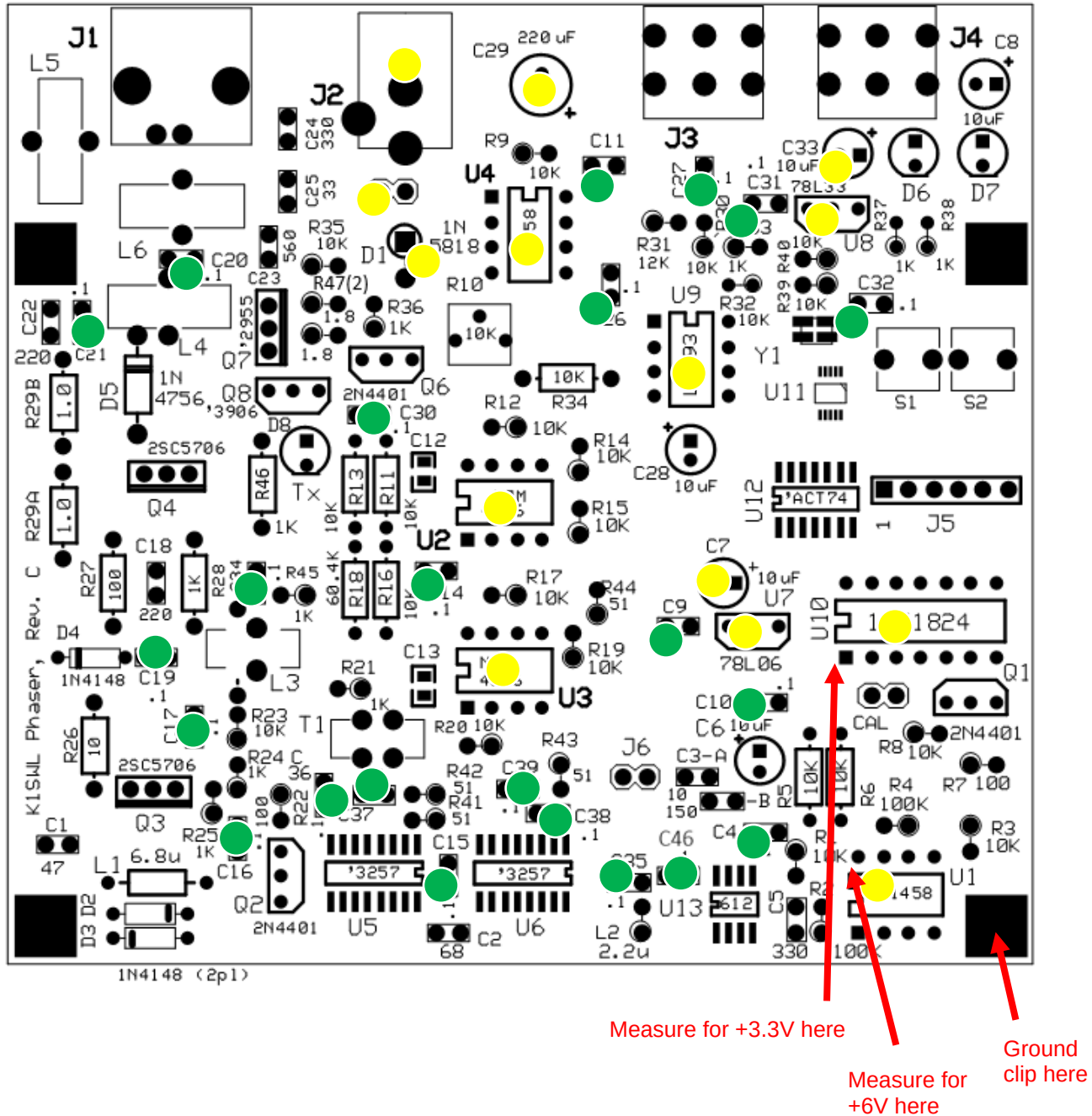
[] **C7, C33:** Install two 10 uF electrolytic caps at C7 and C33. These and all other electrolytic capacitors are polarity-sensitive. In each case, the longer lead must be installed nearest the ‘+’ sign on the silkscreen legend. Double-check to ensure that the negative side of the capacitor (marked with a ‘-’) shown on the side of the capacitor is facing away from the ‘+’ sign on the silkscreen.

[] **C29:** Install the 220 uF electrolytic cap at C29, again observing polarity.

TEST #1: Basic Power

- Apply 12V power through jack J2. (The center pin is positive.)
- With your multimeter ground clip attached to any of the board mounting holes, measure for 6V at pin 8 of the U1 pin socket.
- Then measure for 3.3V at pin 1 of the U10 IC socket.
- Do not proceed until you see these voltages, as the rest of the components depend on these voltages for proper operation.
- Remove power before proceeding to the next assembly group.

Group 1 Component Placement



GROUP 2 ASSEMBLY: T-R Switching

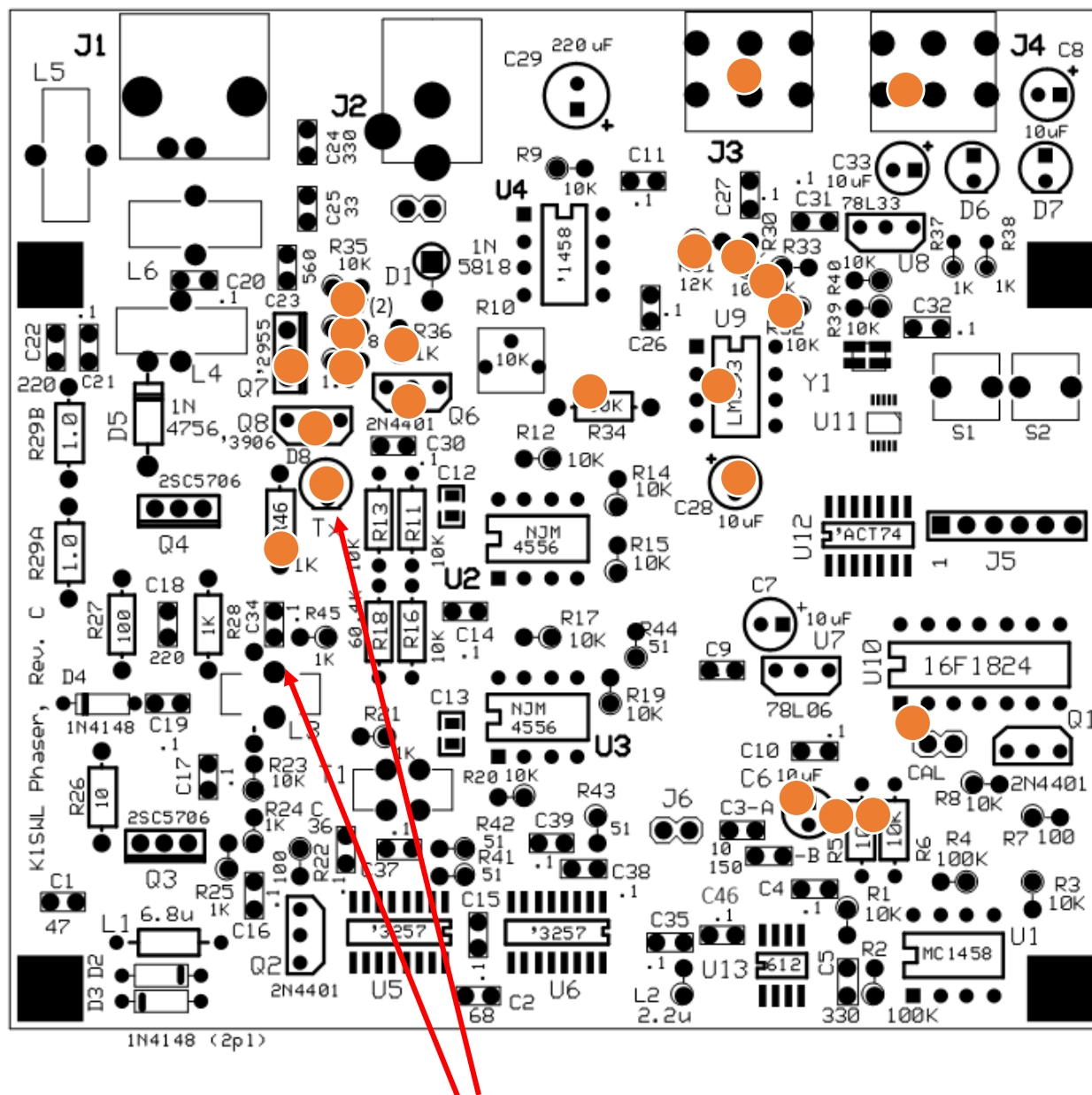
This assembly group installs the T-R switching logic. Component locations are indicated with **orange dots** in the diagram on the next page.

- [] **J3, J4:** Install 3.5mm audio connectors at J3 and J4.
- [] **R5, R6, R30, R32, R34 and R35:** Install 10K resistors (brn-blk-blk-red-brown) resistors at R5, R6, R30, R32, R34 and R35.
- [] **C6, C28:** Install two 10 uF electrolytic caps, at C6 and C28. This and all other electrolytic capacitors are polarity-sensitive. In each case, the longer lead must be installed nearest the ‘+’ sign on the silkscreen legend. Again, double-check the polarity before soldering.
- [] **R31:** Install the 12K (brown-red-orange-gold) resistor at R31.
- [] **R33, R36:** Install 1K (brown-blk-red-gold) resistors at R33 and R36.
- [] **R47A, R47B:** Install 1.8 ohm (brown-grey-gold-gold) resistors at R47A and R47B. These components are unmarked on the silkscreen and are just to the right of Q7.
- [] **Transistor Q6:** Install a 2N4401 3-pin device at Q6. Observe orientation of the device’s flat edge. Take care not to overheat solid-state devices.
- [] **Transistor Q7:** Install the NTD2955 3-pin device at Q7. Observe orientation of the device’s metal tab.
- [] **Transistor Q8:** Install the 2N3906 3-pin device at Q8. Observe orientation of the device’s flat edge.
- [] **Resistor R46:** Install 1K (brown-blk-red-gold) resistors at R46.
- [] **D8 ‘Tx’ LED:** Install the Red LED above the ‘Tx’ legend. Observe the orientation of the device’s flat edge. *The longer of the device’s two leads installs in the upper hole of the two pads.* **Note:** If you plan to install the Phaser in its companion enclosure, do not install this part. Set it aside for later use.
- [] **Integrated Circuit U9:** Insert the LM393 8-pin IC in the socket at U9. Observe orientation of pin 1, as marked with a small dot or cutout at the pin 1 edge of the device.
- [] **2-pin header:** Install the remaining 2-pin male header at the location marked ‘CAL’ near U10.

TEST #2: T-R Switching

- Upon completion of this group, you can connect the 12V power source again to J2 and a cable from your computer audio output at J3.
- Launch the WSJT-X software on your computer and command it to ‘TUNE’. (See “Quick Start” in Appendix 4.)
- Measure the voltage ‘V_{sw}’ (at the top pad of L3, near C34) and see it change from 0V DC to a nominal 12V DC. Verify that the ‘Tx’ LED Illuminates.
- Do not proceed until you see these voltages, as the rest of the components depend on these voltages for proper operation.
- Uncheck the WSJT ‘TUNE’ box and verify that the LED turns off.
- Remove power before proceeding to the next assembly group.

Group 2 Component Placement



Observe the change in voltage from 0V to 12V at top of L3 (and the red LED turning on) when commanding "Tune" in WSJT-X software

GROUP 3 ASSEMBLY: Local Oscillator

This assembly group installs the local oscillator function. Component locations are indicated with **red dots** in the diagram on the next page.

- [] **Green LED:** Install the Green LED at the board location labeled 'FT8'. Ensure that the longer lead (the anode) is installed toward the rear (top) of the board. The shorter lead (the cathode) is identifiable by its proximity to the 'flat' on the otherwise-round base of the diode body. (It may be difficult to see this flat.)
- [] **Yellow LED:** Install the Yellow LED at the board location labeled 'ALT'. (Same caution about orientation.)
- [] **R37, R38 (1K):** Install two 1K resistors (brown-blk-red-gold) at R37 and R38. These resistor labels are not shown on silkscreen but are located next to the green 'FT8' LED.
- [] **R39, R40 (10K):** Install two 10K resistors (brn-blk-blk-red-brown) R39 and R40. These positions are also not on the silkscreen but are located just above Y1.
- [] **S1, S2:** Install tactile switches S1 and S2.
- [] **Integrated Circuit U10:** Install 14-pin IC U10 (PIC16F1824). This device is polarity-sensitive. The dot and/or notch at one end of the device must face to the left as you are viewing the 'U10' designator. You may need to bend the IC pins gently inward to mate with the socket. The best approach is to push the IC down on each side separately using a hard surface to bend one 7-pin row at a time. Once you've installed the IC, ensure that all leads are seated in the socket.
- [] **J6 (2-pin header):** Install a 2-pin male header at the positioned labeled J6.



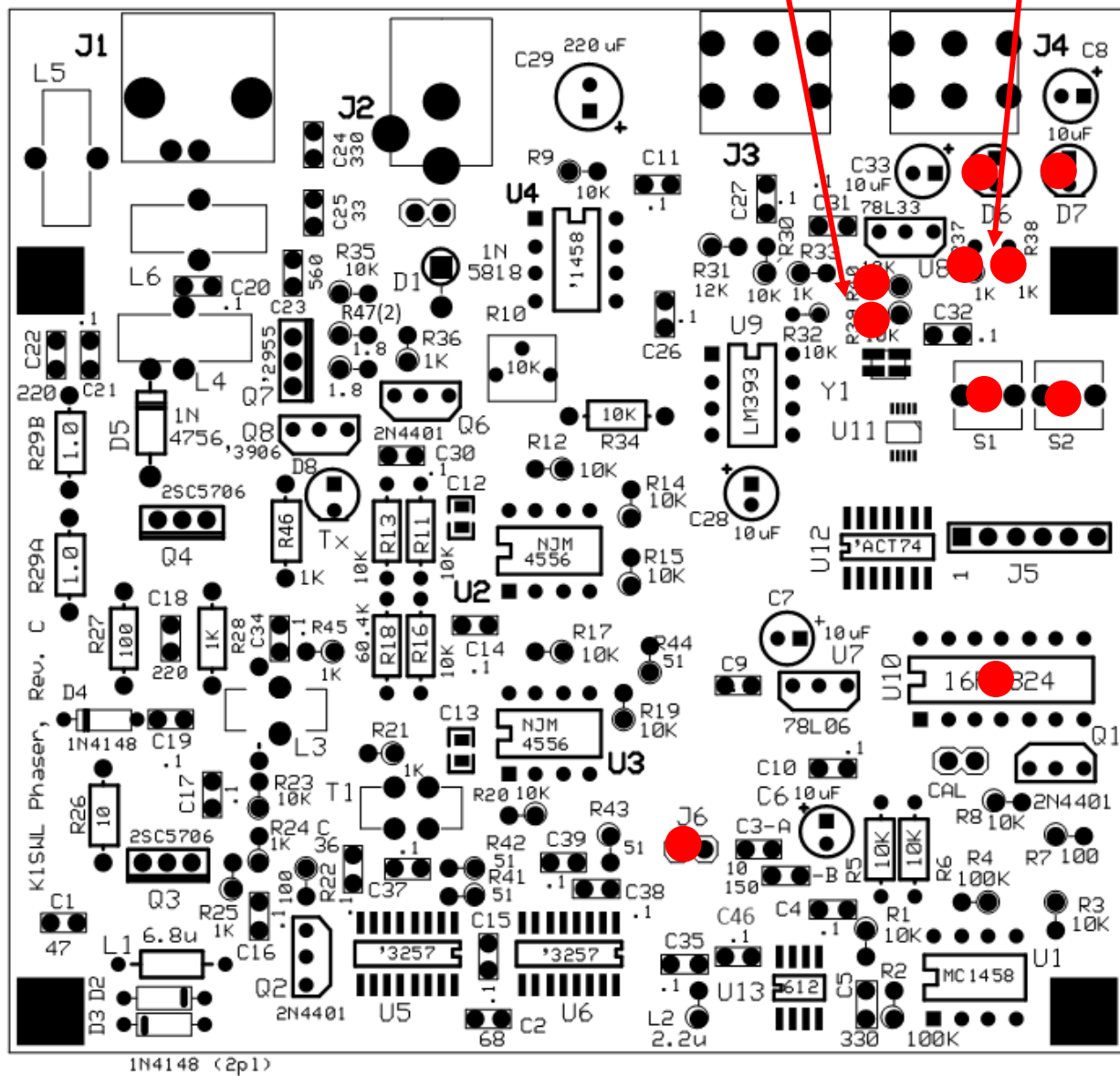
Test #3: Measure the Carrier

Connect a 12V power source to J2, then connect a clip lead to one of the exposed pins on J6. The clip lead serves as an antenna to radiate the clock frequency present on J6. You should be able to hear this steady carrier on your 'big rig' at 10,136.00 kHz. The frequency may be off by several hundred Hz. Refer to the 'Adjustment/Calibration' section of these Instructions for calibration.

Group 3 Component Placement

R39 & R40

R37 & R38



GROUP 4 ASSEMBLY: Receiver

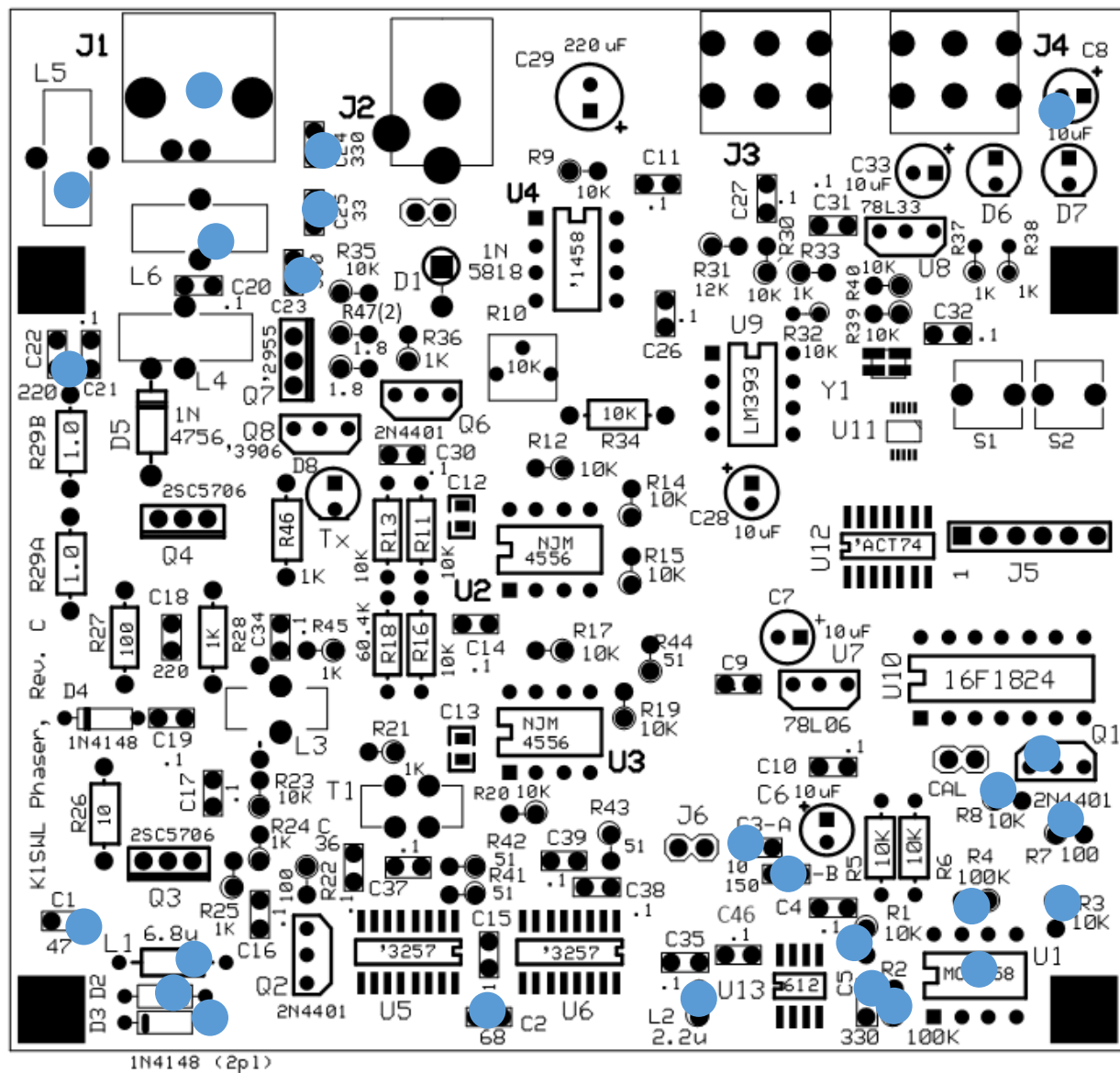
This assembly group installs the Phaser's receiver components. *See next page for component placement, as indicated by blue dots.*

- [] **C1 (47 pF):** Install a 47 pF (marked as '470' or '47J') capacitor at C1.
- [] **D2, D3 (1N4148):** Install 1N4148 diodes (small, glass bodies) at D2 and D3. Match their orientations as shown on the silkscreen.
- [] **C2 (68 pF):** Install 68 pF (marked as '680' or '68J') capacitor at C2.
- [] **C25 (33 pF):** Install 33 pF (marked as '330' or '33J') capacitor at C25.
- [] **C22 (220 pF):** Install a 220 pF (marked as '221') capacitor at C22.
- [] **C3-A (10 pF):** Install a 10 pF (marked as '100' or '10J') capacitor at C3-A.
- [] **C3-B (150 pF):** Install a 150 pF (marked as '151') capacitor at C3-B.
- [] **C5, C24 (330 pF):** Install 330 pF (marked as '331') capacitors at C5 and C24.
- [] **L1 (6.8 uH):** Install a 6.8 uH RF Choke (blue-grey-gold-gold) at L1. The RF chokes are slight larger in diameter than the ¼W resistors and are more tapered at the ends of the body. **Caution:** Avoid sharp bends in the leads right where they leave the body, as this may damage the component.
- [] **L2 (2.2 uH):** Install a 2.2 uH RF Choke (red-red-gold-silver) at L2.
- [] **R1, R3, R8 (10K resistor):** Install 10K (brn-blk-blk-red-brn) 1/4W resistors at R1, R3 and R8.
- [] **R2, R4 (100K resistor):** Install 100K (brn-black-yellow-gold) resistors at R2 and R4.
- [] **R7 (100-ohm):** Install a 100-ohm (brn-black-brn-gold) resistor at R7.
- [] **C8 (10 uF):** Install the remaining 10uF electrolytic capacitor at C8.
- [] **Q1 (2N4401):** Install a 2N4401 transistor at Q1, matching its outline to that of the silkscreen.
- [] **C23 (560 pF):** Install the 560 pF ('5611') capacitor at C23.
- [] **U1 (MC1458):** Install an MC1458 8-pin IC at U1. The notch or dot on the case must face to the left.
- [] **L5 (T37-6 Toroid):** Cut a 10" length of #26 red magnet wire and wind **15 turns** on a T37-6 (yellow) toroid. Prepare the leads and install at L5. **See Appendix 8 for guidelines on winding toroids.**
- [] **L6 (T37-6 Toroid):** Cut a 13" length of #26 red magnet wire and wind **18 turns** on a T37-6 (yellow) toroid. Prepare the leads and install at L6. **See Appendix 8 for guidelines on winding toroids.**
- [] **BNC (J1):** Install the BNC connector at J1. Solder all connections. The two 'press-fit' leads are not used electrically but they provide mechanical stability

Test 4: Receiving FT8 Signals

When this assembly group is complete, the Phaser receiver should be operational. Refer to the Quick-Start section of these instructions for hookup and operating guidance.

Group 4 Component Placement



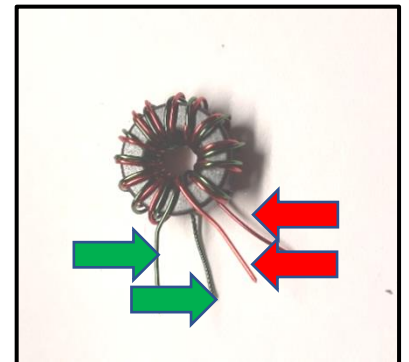
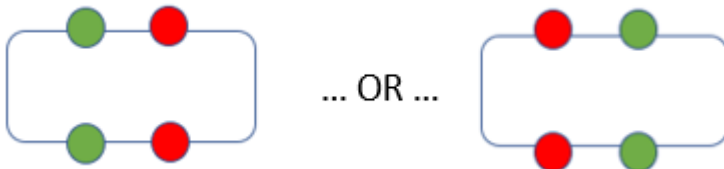
GROUP 5 ASSEMBLY: SSB Phasing

This assembly group installs the SSB phasing components. See next page for component placement, with **red** and **orange** dots indicating component locations.

- [] **R18 (60.4K):** Install the 60.4K 1%-tolerance (blue-black-yellow-red-gold) resistor at R18.
- [] **R9, R11, R12, R13, R14, R15, R16, R17, R19, R20 (10K):** Install 10K (brn-blk-blk-red-brown) resistors. These are highlighted in **red** on the next page.
- [] **R10 (Trimmer):** Install the 10K trim pot (blue, 3 leads) at R10.
- [] **U4 (MC1458):** Install an MC1458 8-pin IC at U4. The notch or dot on the case must face toward the back of the board, near R9.
- [] **R21 (1K):** Install a 1K 5% resistor (brown-blk-red-gold) at R21.
- [] **C36, C37, C38, C39 (0.1 uF):** Install the remaining (4) 0.1 uF capacitors (marked '104') at C36-C39.
- [] **R41, R42, R43, R44 (51 ohm 5%):** Install 51-ohm 5% resistors (green-brown-blk-gold) at R41-R44
- [] **T1 (Toroid FT37-43):** Wind 14 turns of the #28 red/green twisted-pair wire on the FT37-43 toroid form (gray, 0.37"-dia). Take extra care to avoid scrambled turns. Prepare the leads as described next. Refer to Appendix 8 "Winding Toroids."

Two leads of a given color must be arranged to one side of the finished toroid. The two remaining leads must be grouped to the other side of the toroid. This is illustrated in the photo to the right.

Now, install the toroid at T1 in either orientation as shown below.

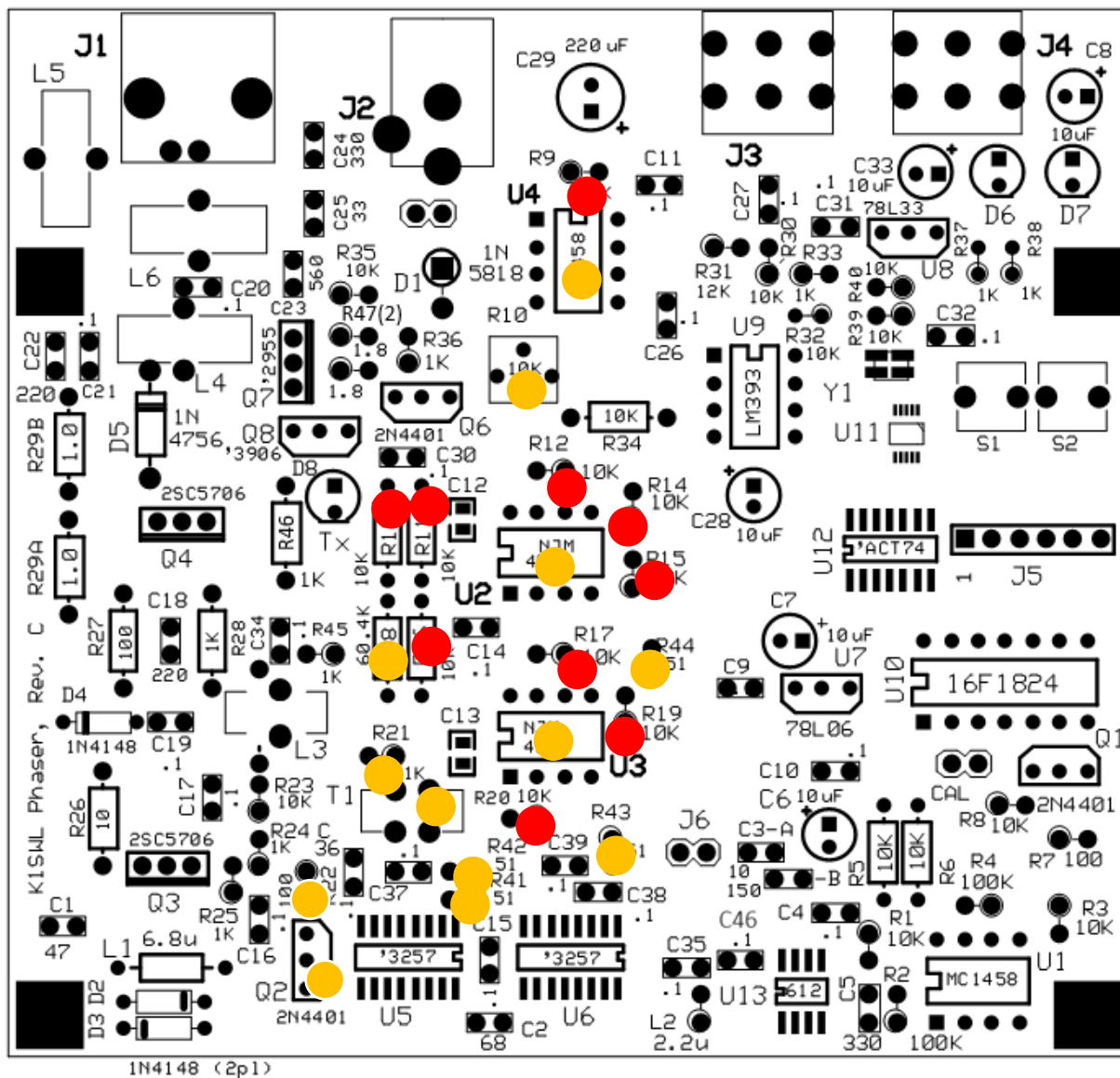


- [] **R22 (100-ohm):** Install a 100-ohm resistor (brown-blk-brown-gold) at R22.
- [] **Q2 (2N4401):** Install the remaining 2N4401 transistor at Q2. Match its orientation to that of the silkscreen.
- [] **U2, U3 (NJM4556):** Insert the NJM4556 8-pin ICs at locations U2 and U3. The notch or dot on the case must face toward the left side of the board, toward C12 and C13.



Test 5: Listening for the Carrier

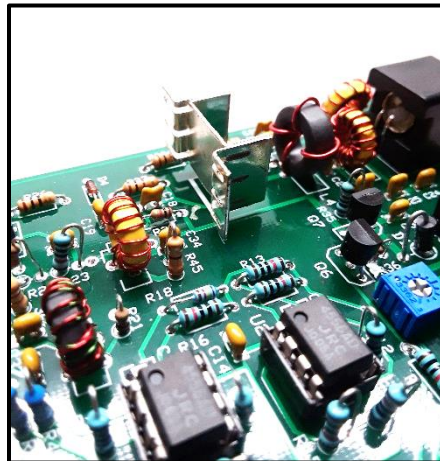
You can connect a clip lead to R22. Leave the other end free. Apply 12V power via J2 and audio via J3. Command WSJT-X to "Tune". (See 'Quick-Start'.) As you tune across the operating frequency with your 'big rig', you'll note that one sideband is much stronger than the other. Opposite sideband at 2400 Hz is down ~35 dB, and noise is ~20 mV pp at this point.

Group 5 Component Placement

GROUP 6 ASSEMBLY: Transmitter Strip

This assembly group installs the Transmitter strip, whereby the mixer output is amplified and sent to the antenna jack.

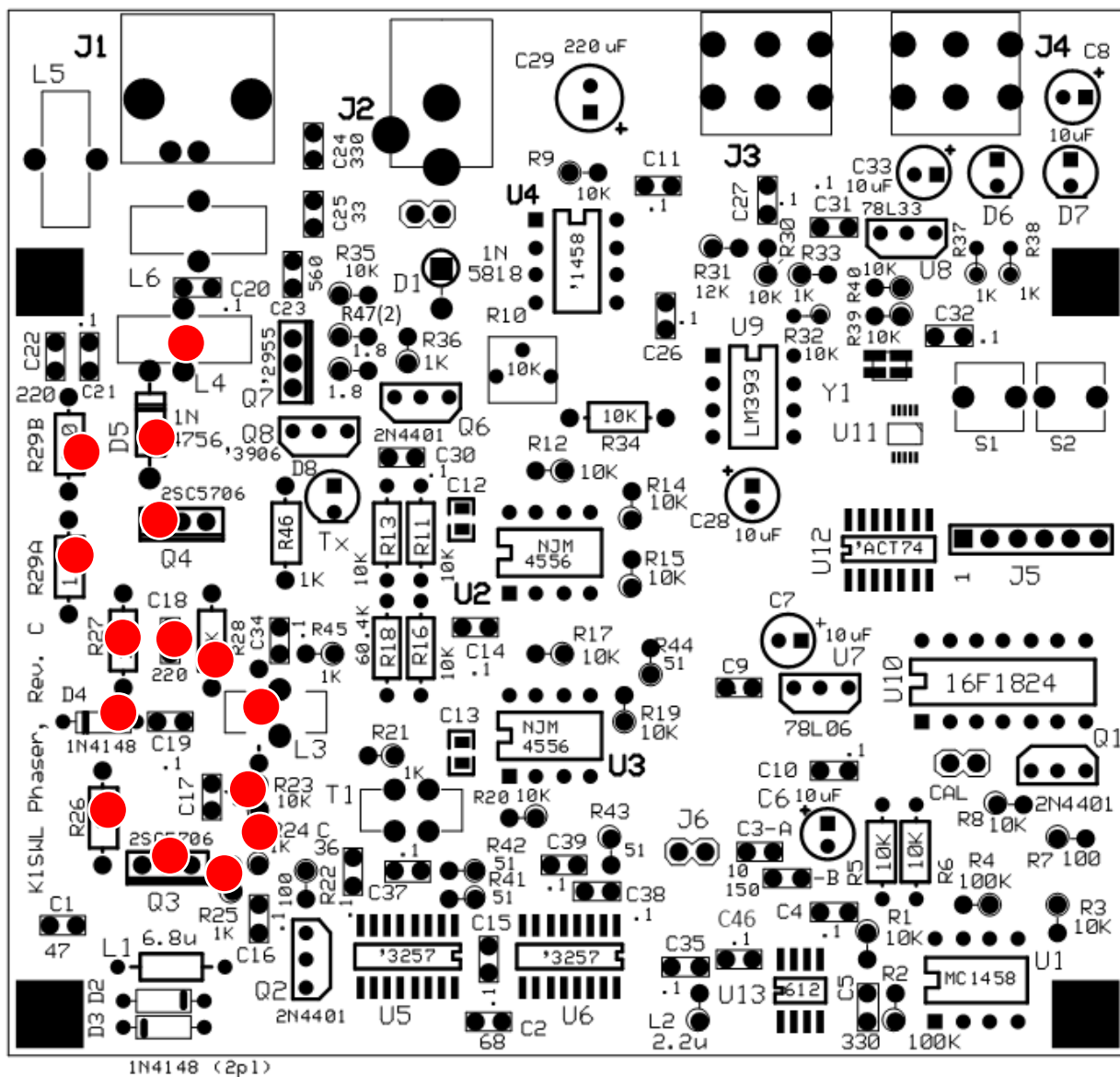
- [] L3 (Toroid T37-6) Cut a 13" length of #26 red magnet wire and wind **18 turns** on a T37-6 (yellow) toroid. Prepare the leads and install at L3. (**NOTE:** the extra pair of pads and silkscreen outline at L3 are unused. They are reserved for use on lower-frequency bands.)
- [] C18 (220 pF): Install the 220 pF capacitor ('221') at C18.
- [] R23 (10K): Install the remaining 10K resistor (brown-blk-orange-gold) at R23.
- [] R24, R25, R28 (1K): Install the remaining (3) 1K resistors (brown-blk-blk-red-brown) at R24, R25 and R28.
- [] R27 (100-ohm): Install the remaining 100-ohm resistor (brown-blk-brown-gold) at R27.
- [] R26 (10-ohm): Install the 10-ohm resistor (brown-blk-blk-gold) at R26
- [] D4 (1N4148): Install the remaining 1N4148 diode at D4. Match the orientation to that shown on the silkscreen.
- [] D5 (1N4756): Install the 1N4756 diode (glass, larger body) at D5. Match the orientation to that shown on the silkscreen.
- [] R29A, R29B (1 ohm): Install the two 1-ohm resistors (brown-blk-gold-gold) at R29A and R29B.
- [] Q3 (2SC5706): Install the 2SC5706 transistor at Q3. Make sure the tab on the device matches the orientation shown on the silkscreen
- [] L4 (Toroid FT50-43): Wind 5 turns of the heavier-gauge magnet wire on the remaining toroid form. Prepare the leads and install at L4.
- [] Q4 & Heatsink Assembly (2SC5706): The 2SC5706 PA device is supplied with its heatsink pre-installed. (*You're welcome!*) Install the assembly at Q4, ensuring that the heatsink fins are oriented as shown below.



This completes the assembly of the FT8 Phaser board kit!

We recommend cleaning the flux residue from the underside of the circuit board. You may use cotton swabs dipped in acetone (a hardware store item). It does a great job removing the flux. *Do not use nail polish remover for this job as it may contain oil or other ingredients.*

Group 6 Component Placement



APPENDIX 1: Parts List (for the Phaser-30)

Quantity	Ref. Designator	Description	Notes & Markings
----------	-----------------	-------------	------------------

Capacitors:

1	C3a	10 pF	'100'
1	C3b	150 pF	'151'
1	C5	330 pF	'331'
23	C4, C9-C11, C14-C17, C19-C21, C26, C27, C30-C32, C34, C35-C39, C46	0.1 uF	'104'
5	C6, C7, C8, C28, C33	10 uF	Polarized
1	C29	220 uF	Polarized

Resistors are $\pm 5\%$ tolerance $\frac{1}{4}$ watt unless otherwise noted:

2	R29A, R29B	1 ohm	Brown-black-gold-gold
2	R47A, R47B	1.8 ohm	Brown-grey-gold-gold
1	R26	10 ohm	Brown-black-black-gold
4	R41-R44	51 ohm	Green-brown-black-gold
3	R7, R22, R27	100 ohm	Brown-black-brown-gold
9	R21, R25, R24, R28, R33, R36-R38, R46	1K ohm	Small- on cut tape Brown-black-red-gold
22	R1, R3, R5, R6, R8, R9, R11-R17, R19, R20, R23, R30, R32, R34, R35, R39, R40	10K 1%	Brown-blk-blk-red-brown)
1	R10	10K	Potentiometer, Blue, 3 leads
1	R31	12K	Brown-red-orange-gold
1	R18	60.4K 1%	Small Blu-blk-ylw-red-brown
2	R2, R4	100K	Brown-black-yellow-gold

Connectors:

3	CAL, Power, J6	2-pin male header, 0.1"	
2	CAL, Power	2-pin female jumper	
1	J1	BNC jack, right-angle mount	
1	J2	DC power jack	
2	J3, J4	Audio jacks, 3.5mm 3-cond.	

Inductors:

1	L4	FT50-43, 5 turns, #22 wire	Grey toroid, larger core diameter
1	T1	FT37-43 toroid, 14 turns bifilar, red/green #28 twisted pair	Grey toroid, smaller core diameter

Semiconductors:

1	D1	1N5818	Black body
3	D2, D3, D4	1N4148	Glass body, small
1	D5	1N4756	Glass body
1	D6	Green LED	
1	D7	Yellow LED	
1	D8	Red LED	
3	Q1, Q2, Q6	2N4401	TO-92, 3 leads
1	Q3	2SC5706	3 leads, small, w/ tab
1	Q4	2SC5706	3 leads w/ pre-installed heatsink
1	Q7	NTD2955	3 leads, small, w/ tab
1	Q8	2N3906	TO-92, 3 leads
2	U1, U4	MC1458P	8-pin DIP IC
2	U2, U3	NJM4556	8-pin DIP IC
1	U7	78L06	TO-92, 3 leads
1	U8	78L33	TO-92, 3 leads
1	U9	LM393	8-pin DIP IC
1	U10	16F1824	14-pin DIP IC

Miscellaneous:

1	--	14-pin IC socket	
5	--	8-pin IC socket	
2	S1, S2	tactile pushbutton switch	
1	--	#28 red/green twisted pair	8", pre-twisted
1	--	#26 magnet wire	48" length
1	--	#22 magnet wire	8" length
4	--	bumper, adhesive	clear plastic, place in 4 corners of pcb

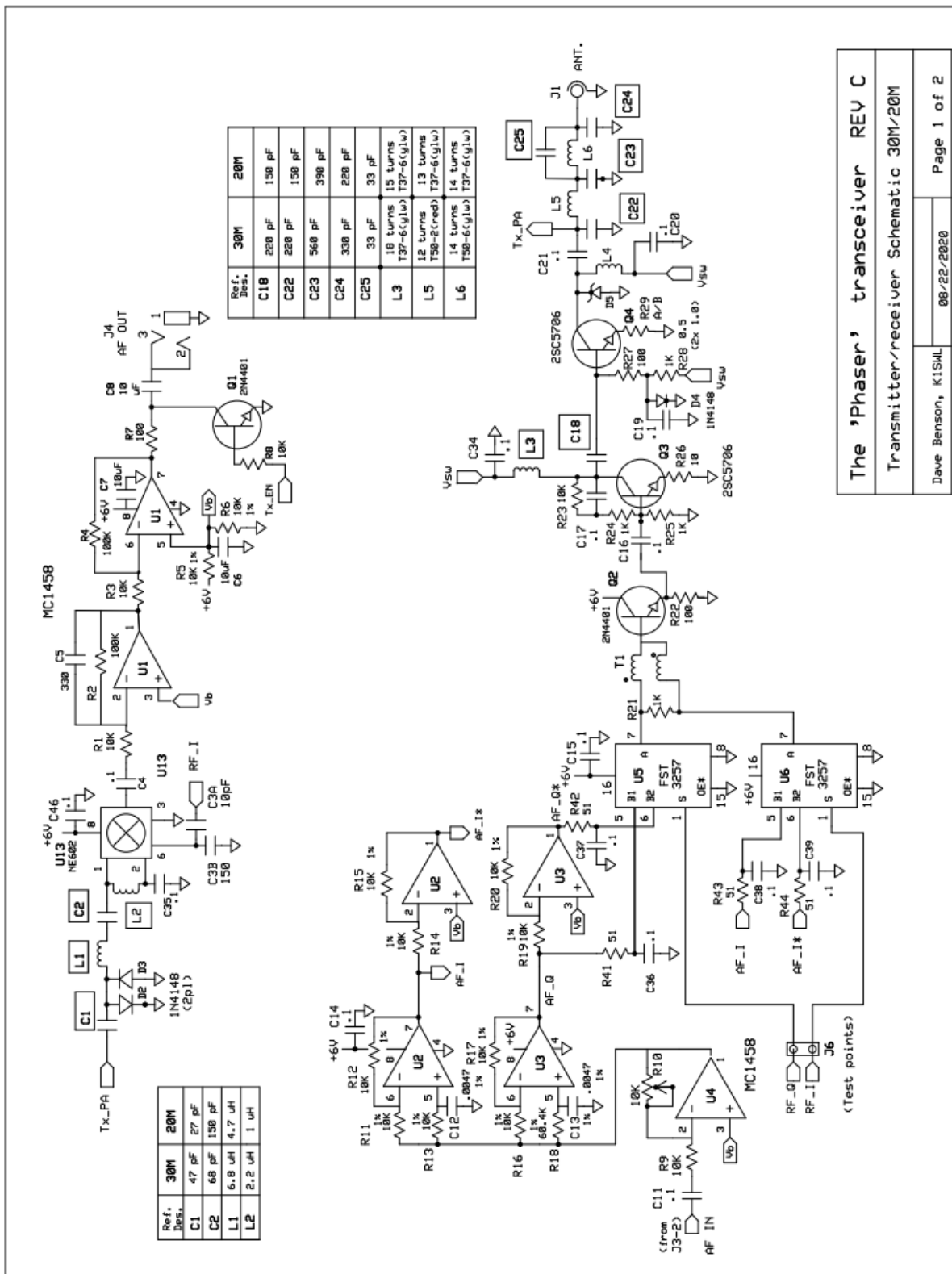
PCB (with pre-installed parts):

1	--	PCB	4" x 4" (approx)
2	C12, C13	4700 pF 1%	(pre-installed)
2	U5, U6	FST3257	(pre-installed)
1	U11	Si5351	(pre-installed)
1	U12	74ACT74	(pre-installed)
1	U13	SA612AD	(pre-installed)
1	Y1	25.000 MHz	(pre-installed)

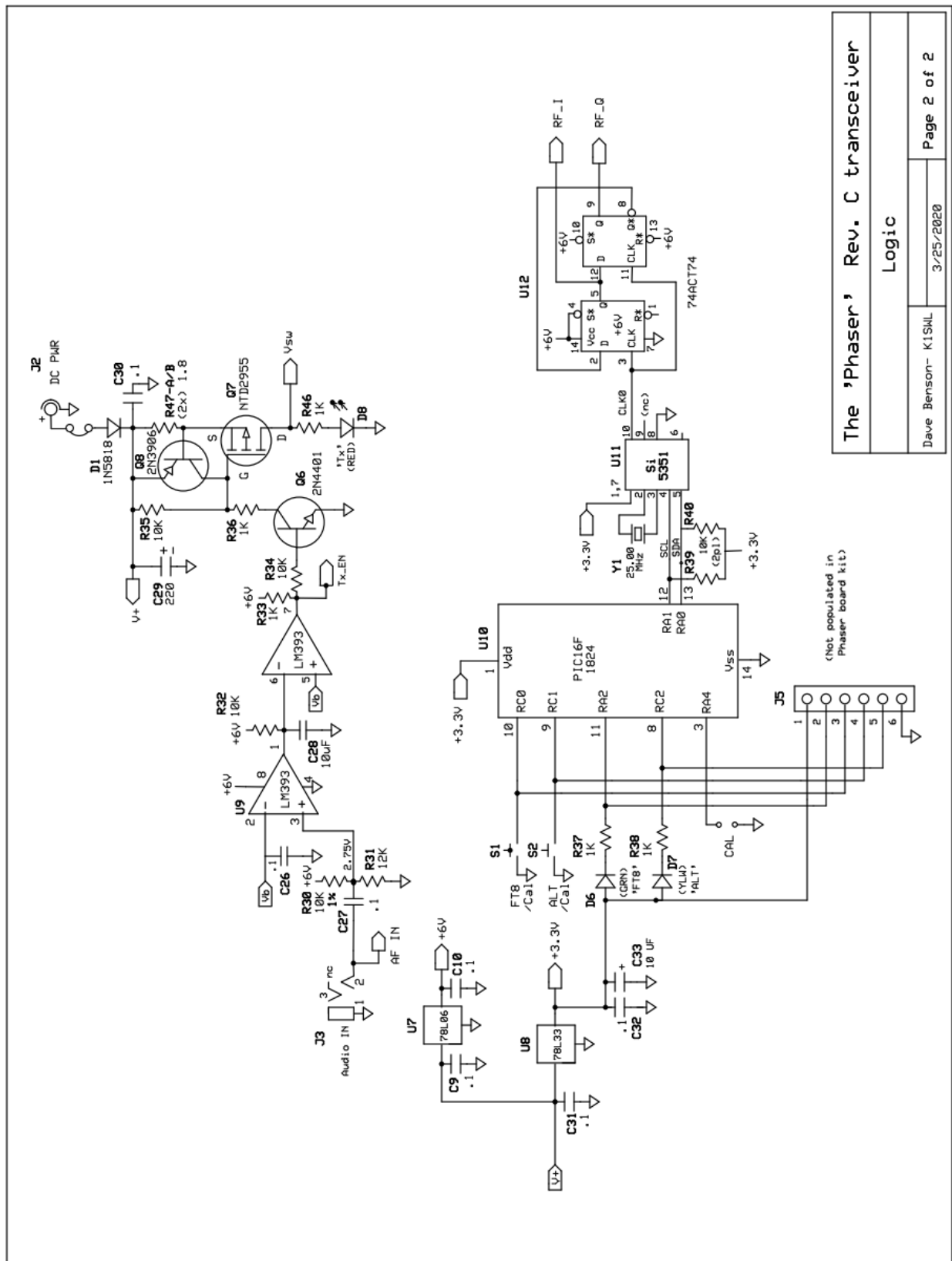
30 meter-specific parts

1	C1	47 pF	'470'
1	C2	68 pF	'680'
1	C25	33 pF	'330' or '33J'
2	C18, C22	220 pF	'221'
1	C23	560 pF	'561'
1	C24	330 pF	'331'
1	L1	6.8 uH	blu-gry-gold-gold
1	L2	2.2 uH	red-red-gold-gold
1	L3	T37-6 toroid. Use 13" of #26 wire to make 18 turns .	Yellow core. See group 6 instructions.
1	L5	T37- 6 toroid. Use 10" of #26 wire to make 15 turns .	Yellow core. See group 4 instructions.
1	L6	T37-6 toroid. Use 13" of #26 wire to make 18 turns .	Yellow core. See group 4 instructions.

APPENDIX 2a: Phaser-30 Schematic: (1 of 2)



APPENDIX 2b: Phaser-30 Schematic: (2 of 2)



The 'Phaser' Rev. C transceiver

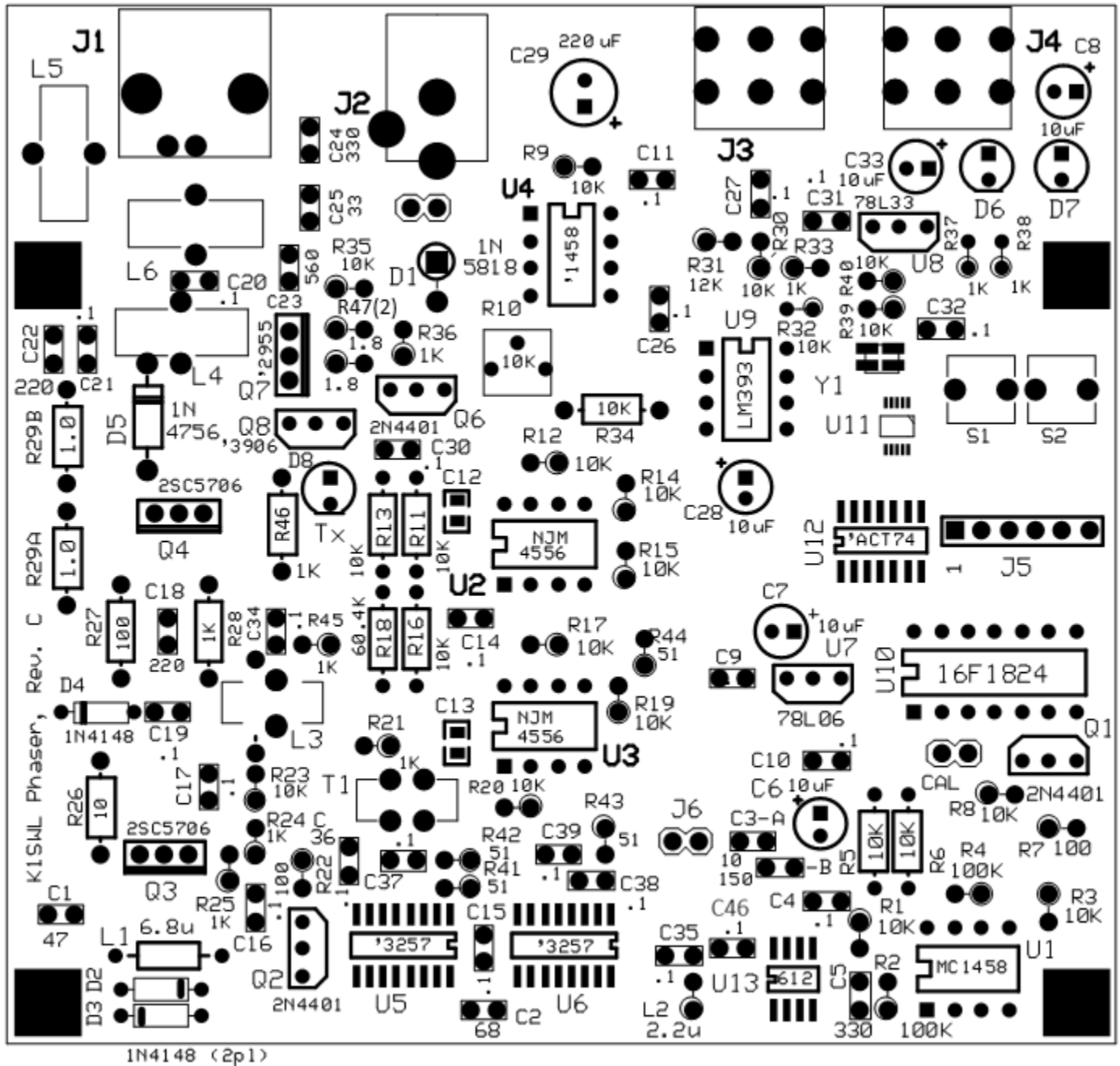
Logic

Dave Benson- K1SWL

3/25/2020

Page 2 of 2

APPENDIX 3: Parts Layout



APPENDIX 4: ‘Quick-Start’ Operation Guide

FT8 operation is based on a computer software application called WSJT-X. This application is available in Windows, Mac and Linux versions. Here’s a link to the definitive Guide to get you started:

<http://www.physics.princeton.edu/pulsar/K1JT/wsjsx-doc/wsjsx-main-2.0.0.html>

Getting on the air with WSJT-X isn’t difficult. The process follows this sequence:

- Download and Install WSJT-X
- Enter your Call sign and Grid Square into a setup dialog box
- Select from several operating options

These steps are well documented in the online ‘Helps’ available from the URL above, and especially in the “FT8 Hinson Tips” guide at https://g4ifb.com/FT8_Hinson_tips_for_HF_DXers.pdf

FT8 relies on fairly accurate timekeeping. Transmissions follow a 15-second pattern, starting at the beginning of the 15-second interval. Transmissions last for 13 seconds. Following a transmission, users are receiving for the next 15-second frame. During the receiving period, your computer is gathering all signals within the receiver passband for 13 seconds. In the remaining 2 seconds, your computer is analyzing the received content and displays decoded text on the left side of the WSJT-X display screen.

To be successful with FT8, you need to ensure that your computer timekeeping is accurate. The Windows timekeeping (for instance) is probably not adequate. The WSJT-X guide recommends an application, and we use ‘Dimension4’ software. It’s free.

<http://www.thinkman.com/dimension4/download.htm>

The software lists a multitude of timekeeping providers. Chose a publicly-available one near you. You’ll also need to choose how often the program syncs your computer. Every 20-30 minutes should be fine.

The need for accurate timekeeping often confounds newcomers to this mode. If you’re seeing good signals and clear FT8 traces.... but no decodes – make sure your timekeeping application is running. *Tip: There’s an option to make the time-syncing a startup task, so it’s always running in the background.*

System Set-up with the Phaser-30 SSB FT8 Transceiver:

Connect to the Phaser as shown in the diagram below.

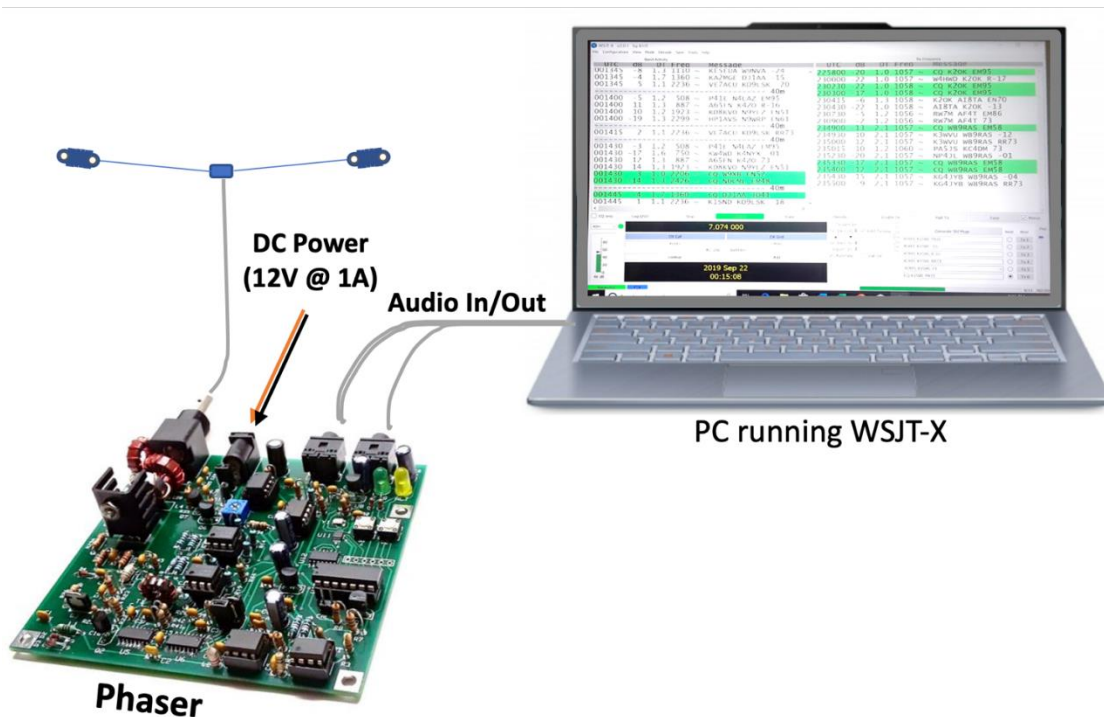


Figure 4: Connecting the Phaser Board to a Computer

Audio Cables -- Be sure the audio cables are going to the correct I/O jacks on your computer by following the signal flow as indicated by the arrows for 'Audio In' and 'Audio Out'.

Power Supply – Use a sufficiently capable supply to power the Phaser board. Oftentimes, ac adapters (e.g., 'wall-warts') do not have good regulation at their current rating, and the transmitted signal will suffer if there is low voltage or AC ripple.

Antenna – The Phaser is expecting to see a matched, 50-ohm antenna feedline at the BNC port. Use a tuned antenna or a pre-adjusted antenna tuning unit (ATU).

'Mic' Setting on Computer – The receive audio being sent to the computer is fairly strong and you will need to reduce the computer 'mic' input level to a near-minimum setting. Open your computer's 'Control Panel' and make the following selections in sequence: 'Hardware and Sound' ... 'Sound' ... 'Manage Audio Devices' ... 'Recording' ... 'Microphone' 'Levels'. *[This sequence may vary somewhat with different operating systems.]* Adjust the slider to have the WSJT receive level bar indicator read approximately 40 dB when your antenna is connected to the board. (See lower-left corner of Fig 5 below for this indicator.)

'Speaker/Volume' Setting on Computer – The computer 'speaker' sidebar sets the audio level delivered to the Phaser board, and together with the 'Pwr' sidebar on the WSJT-X control screen, it serves as the RF 'power adjust'. A good setting to use as a starting point is mid-scale on both of these controls.

If your computer has a single 4-pin jack instead of separate ‘mic in’ and speaker/headset jacks, there’s a solution. There are several Y-adapters available:

Here’s one that appears to be readily available:

<https://www.walmart.com/ip/3-5mm-Headset-Adapter-Y-Splitter-Jack-Cable-with-Separate-Microphone-and-Audio-Headphone/521294778>

It splits the laptop’s 4-pin connection into a pair of separate 3-pin audio and microphone connections. You’ll still need a pair of 3.5mm 3-pin cables, but you probably have those from your PSK31 days.

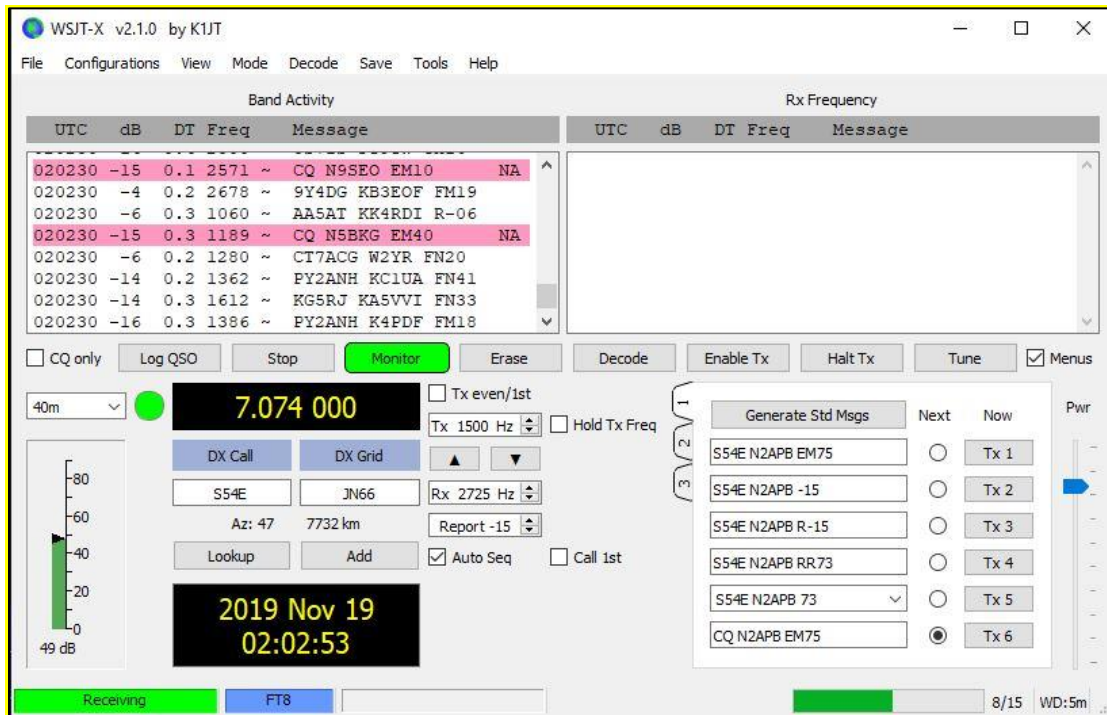


Figure 5: Sample view of WSJT-X control screen

APPENDIX 5: Adjustment/Calibration

There's not much to it!

Adjustment:

- Turn trim pot R10 fully counterclockwise.
- Connect cables to J1 through J4 as described in the previous section (Appendix 4). Open the WSJT-X application on your computer.

NOTE: This step assumes you have an SWR/power meter between the Phaser and the antenna or dummy load.

- Set the WSJT-X 'Pwr' slider (lower right corner of display) to minimum.
- Connect an oscilloscope probe or multimeter (select volts DC) to the right end of R34. (To right of blue trimpot)
- Click on the 'TUNE' button on your computer's display monitor. Advance the Pwr slider until the multimeter/scope reading changes to 4-5V DC. Advance the Pwr slider 5-10% of full-scale further.

Click the TUNE button on and off a few times to verify that the computer's audio is reliably detected by the Phaser.

It's tempting to set the Power slider to maximum, but there's a reason not to: Your computer's sound card output may have noticeable distortion content at its maximum setting. It's best to run the sound card output at a moderate level to keep your on-the-air signal as clean as possible.

- With TUNE selected, Advance trim pot R10 clockwise for an output power reading of up to 4 Watts into a 50-ohm load or matched antenna. **Caution: DO NOT EXCEED 4W OUT. AVOID EXTENDED TESTING. Although brief excursions above 4W are harmless, continuous operation at maximum power may result in damage to Q7 and possibly the PA device Q4)**
- **Once further increases in drive have no effect on power output, STOP!** Back the drive down to a value right at the point where it quits having an effect.
- Click on 'TUNE' again to end the test.

Calibration:

Apply 12 volts DC via J2. The other cables are not needed.

Install the remaining 2-pin jumper at 'CAL' (just below U10, the 14-pin DIP IC). This puts the logic in Frequency Calibration mode.

If you have a frequency counter:

- Connect a probe to one of the two pins on J6 and observe the displayed frequency. (J6 has the two jumpers attached and two open pins. Refer to the photos in Assembly Group 3 on page 12.)
- Pressing S1 lowers the operating frequency and pressing S2 raises the frequency. Adjust the frequency to 10,136.00 kHz. There's plenty of time to perform this adjustment. NOTE: During the interval when the logic is updating the Si5351, that device's output is turned off. The result is that the frequency will read somewhat low. Wait several seconds after letting up on S1 or S2 to let the reading stabilize.

No frequency counter?

- Connect a clip lead to one of the two pins on J6 and set your 'big rig' to 10,136.00 kHz. Leave the other end of the clip lead free. You should be able to hear a steady carrier from the Phaser. Adjust frequency with S1 and S2 until the carrier's pitch matches that of your big rig's (key-down) sidetone.
- **No 'big-rig'?**
 - Install all cables to the Phaser as described in the previous section (Quick-Start). You should see the characteristic waterfall traces of FT8 signals. Adjust frequency with S1 and S2 until significant activity starts at about 500 Hz on the WSJT-X waterfall display. There'll be more activity during nighttime hours.
 - Once you've set the frequency by any of the methods above, remove the jumper from the 'CAL' location. This commands the logic to store the adjusted frequency in non-volatile memory and the Phaser returns to normal operation. This calibration should not need to be repeated.

APPENDIX 6: Theory of Operation

Receiver: The receiver input is supplied from a connection ('Tx PA') on the collector side of the low-pass filter. The combination of C1 and L1 provides a fairly broad peak at 10.1 MHz for a measure of selectivity. C2 and L2 provide rejection of AM broadcast energy, with L2 also providing equal DC biases to the receiver mixer inputs. An NE602 (U13) provides the mixer function. *When signals are mixed with another signal, the resulting product includes sum and difference frequencies. We're interested in the difference frequency; in this case, audio.*

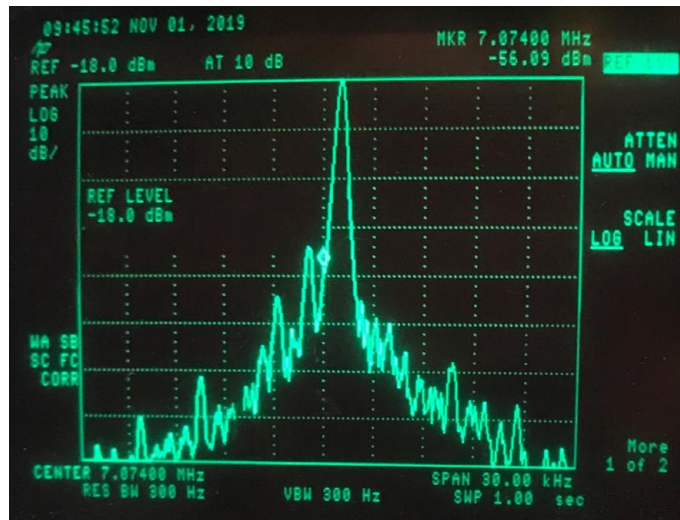
The resulting audio is amplified by a gain of 100 (40 dB) in the two sections of U1. Note that the output level is quite low. *The audio level is inadequate for headphone use but is perfectly adequate for a sound card's microphone input.*

Transmitter: The frequency source for both transmitting and receiving is a Silicon Labs Si5351 (U11). This IC outputs a 3V square wave at four times the operating frequency. Two flip-flops comprising U12 divide the frequency by a factor of 4 and provide a reference phase ('I') and a quadrature phase ('Q') delayed 90 degrees from the reference.

A dual op-amp (U2) generates complementary (180° apart) signals from audio supplied by the host computer sound card. U3 has the same function, but its complementary audio outputs are delayed in phase by a nominal 90 degrees with respect to U2's outputs. U4 and adjustment pot R10 set the signal level applied to the transmit strip. *This adjustment allows setting the output power without needing to tinker with the WSJT-X sound card settings. This ensures reliable T-R operation even at very low power levels, should you choose to do that.*

The two sets of audio signals, 0 degrees ('I') and 90 degrees ('Q'), are applied to the inputs of 2:1 multiplexers U5 and U6. The RF inputs to these devices are at the 'S' (Select) inputs and are also in quadrature. The two multiplexer output signals each consists of a double-sideband waveform. The two signals are combined by T1 to form a single-sideband signal.

Suppression of the unwanted sideband was measured as 35 dB-or-better over an audio frequency range of 800-2800 Hz.



Transmitted 1-kHz USB tone.

Note suppression of both carrier and opposite sideband.

(The carrier frequency is marked by a diamond symbol at mid-screen.)

Measurements were taken with a homebrew transceiver chosen for its lack of AGC. At low audio frequencies, both sidebands were present in the audio output due to IF bandwidth limitations. Lower tone-frequency suppression can be

expected to be worse. The phasing network design is from Experimental Methods for RF Design , Figure 9.48. This network was scaled up in frequency for the Phaser by reducing the values of capacitors C12 and C13.

An emitter-follower (Q2) buffers the SSB signal, and feedback amplifier Q3 provides sufficient gain to drive the PA stage. Collector choke L3 and associated capacitor C18 form an L-C network. This transforms the stage's 200-ohm design value to the lower 20-ohm nominal input impedance of the PA stage. The PA stage collector impedance is designed for 18 ohms. The output low-pass filter provides an impedance-step-up to 50 ohms in addition to cleaning up the collector waveform. Capacitor C25 provides a notch near the filter's 2nd harmonic response. This allows the relatively-simple low-pass filter to meet FCC/EU standards for spectral purity.

Logic: The controller IC (U10) is a PIC 16F1824 running on an internal 16 MHz clock. Upon application of power, the PIC initializes the Si5351 to the FT8 operating frequency. In normal operation it's in a polling loop looking for closures on frequency-select switches S1 and S2. Upon sensing a closure, it re-initializes the Si5351 to the appropriate frequency and illuminates the corresponding discrete LED.

Transmit/Receive switching is done by sensing the presence of audio from the computer sound card. An LM393 comparator output shorts capacitor C28 to ground for audio input signals greater than 450 mV p-p . The second comparator section is used solely as an inverter and turns Q6 and Q7 on to serve as a bias source for transmit operation.

Frequency Calibration: The PIC controller polls the 'CAL' input and detects when the user puts a jumper in place. The controller then enters a process of changing the Si5351 reference signal slightly based on the user's pressing the S1 and S2 to decrease or increase (respectively) the operating frequency. The frequency correction is stored in non-volatile memory upon removal of the CAL jumper. This is a one-time procedure. See 'Adjustment/ Calibration' (Appendix 5).

APPENDIX 7: User-Programmable ALT Frequency Entry

The ALT frequency may be programmed to be anywhere in the HF spectrum. This supports other digital operating modes and the use of the Si5351A as a general-purpose signal source. The signals present on the 2x3 male pinheader at the 'REV' board location are 5V TTL/CMOS compatible logic signals that may be taken off the Phaser board by means of a suitable mating connector.

Here's how to program the ALT frequency:

But first note: About halfway through kit production we changed the number of digits to be entered via Morse when programming the ALT.

➔ If you have a Phaser s/n **before 776**, you will be entering **five kilohertz digits** per the description below.
Example: enter '0 7 0 3 8' for 7,038 kHz.

➔ If you have Phaser s/n **776-or-higher** you will instead be entering **six digits**, which is five kilohertz digits **plus** the 100-Hz digit. Example: enter '0 7 0 3 8 5' for 7,038.5 kHz. This extra digit entry better enables one to program the ALT frequency for use with digital modes like WSPR.

And now the procedure! We'll use 5-digit entry for illustration purposes, but you may be entering six digits as described above:

- 1) Press and hold the FT8 pushbutton during power-up.
- 2) Upon releasing the pushbutton, the FT8 LED blinks on and off TWO times.
- 3) Enter any **5**-digit frequency (i.e., only the kilohertz digits) between 00100 and 30000, as follows:
 - a) Use the FT8 and Alt pushbuttons to enter the frequency using Morse characters;
Press the FT8 pushbutton for a DIT and the Alt pushbutton for a DAH;
 - b) Enter Morse elements at any speed and element spacing. 'Slow is good'; and
 - c) After each DIT is entered, notice the FT8 LED blink on and off ONCE;
After each DAH is entered, notice the Alt LED blink on and off ONCE.
- 4) After **five** kilohertz digits are entered the Phaser will:
 - a) Blink the Alt LED on-and-off FIVE times;
 - b) Save the new frequency in EEPROM for future use (as the Alt frequency); and
 - c) Illuminate the ALT LED to indicate that this new frequency is in effect. *Operation of the FT8 frequency is unchanged.*

If there is an error during entry of the Alt Frequency, the Phaser will blink the Alt LED continuously until power is removed, at which point you may try again.

APPENDIX 8: Winding Toroids

A properly-wound toroid is shown at right.

- **Each time the wire goes through the hole it counts as a turn**
- **The windings are tight.** Pull the wire taut after each turn comes over the outer edge of the core.

and please... no scrambled turns!



GOOD TOROID!

- **Doublecheck the turns count.** *I do this by bumping a fingernail over each turn.* When the number of turns is correct, cut the leads to a length of 3/8" (1cm).
- **Strip the leads** by scraping with a small knife to remove the insulation. *(Despite the manufacturer's claims, the insulation will probably not melt when you apply a soldering iron.)*
- **Once the leads are prepared, make sure the turns are evenly spaced on the toroid form.** *If the leads are bunched together, the inductance will be too high. This results in low power output.*



BAD TOROID!

(bad bad toroid!)

CREDITS

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Kitting ... *Larry Przyborowski, K3PEG*

Productization, Documentation, Financing, Sales, Website, Support ... *George Heron, N2APB*

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DOCUMENT REVISION HISTORY

Rev A.8 – Baseline

Rev A.9 – Added revision date to cover page. Fixed typo in Group 6 Assembly section for L3: “Prepare the leads and install at L3.” Clarified length of magnet wire in Parts List for L5 and L6.

Rev A.10 – Page 5, Specs, at the end of the page on Kits, 17 meters was missing. Page 21, Parts List, R23 (10K) added, with 10K parts count now correct at 22. Generic version of Figure 4 added on page 29. Red dot by R9 on Page 17.

Rev B – Internal use; not released.

Rev C – Documents effective with Round 4 (orders #300-up) where new ‘Rev C’ Phaser PCB we introduced.

Rev C.0 – Numerous changes reflected in the new ‘Rev C’ pcb for Round 4 production:

- 1) Added Tx indicator LED and current limit resistor, providing clear onboard indication of when the Phaser is in Tx mode;
- 2) J6 changed to 2-position pinheader for test point purposes only and the two associated jumpers were eliminated, as sideband selection is no longer necessary (always USB);
- 3) Replaced DIP-version NE602 with factory-installed surface mount SA612AD to improve buildability and reliability;
- 4) Replaced Q7 (a 2A-rated TO-92 Q7 device) with a 12A-rated TO-220 packaged device (NTD2955) to better sustain higher-than-recommended RF power levels;
- 5) Added Q8 and R47A/B to serve as an 800-900mA current-limit function for the Vsw bus to the Tx final, further protecting the Q4 final from overdrive failure; and
- 6) Replaced 5V regulator U7 with a 6V regulator to improve Tx audio signals (minimize distortion) at higher drive settings. *The logic ICs (U12, U5 and U6) are rated for a maximum 7V supply and are unaffected by this change.*

NOTE: Changes 4,5 and 6 above are all related to ‘Turning the Amp up to 11’ (i.e., attempting to overdrive the Tx audio input to the Tx chain which can lead to Q4 and Q7 failures.) While it is tempting, things do not improve when pushing the audio drive all the way up. Available output power is unaffected by these changes - the goal is improved reliability.

Rev C1 – The ‘C40’ designator on the pcb silkscreen (next to U13 in lower right) should be ‘C46’. Similarly, the designators for R46 and R47 are swapped on the pcb silkscreen. These has been corrected in all Component Placement diagrams in this Instructions document, on pages 9, 11, 13, 15, 17, 19 and 25. The green dot locator on the Group 1 Component Placement was moved from the upper-left (near C25) to be next to C30 (below Q6). Follow these Component Placement diagrams in the manual for putting the parts onto the pcb.

Rev C2 – L5 and L6 toroid cores (type 6, yellow) and turn counts (15 and 18, respectively) were corrected, both in the Parts List and in the Group 6 assembly instructions.

Rev C3 – L6 reference in Group 4, page 14, corrected to T37-6 (yellow).

Rev C4 – All 23 0.1 uF caps installed at once in Group 1.

Rev C5 – R45 references removed (unnecessary in this Phaser board/band.)

Rev C6 – Added description of 6-digit programming of the ALT frequency in Appendix 7.

Rev C7 – Misc typos